

# SMART Transmitter Power Supply KCD2-STC-1.SP

- 1-channel signal conditioner
- 24 V DC supply (Power Rail)
- Input for 2-wire SMART transmitters and current sources
- Output for 4 mA ... 20 mA or 1 V ... 5 V
- Housing width 12.5 mm
- Connection via spring terminals with push-in connection technology
- Up to SIL 2 acc. to IEC/EN 61508

# CESIL2 HART

#### **Function**

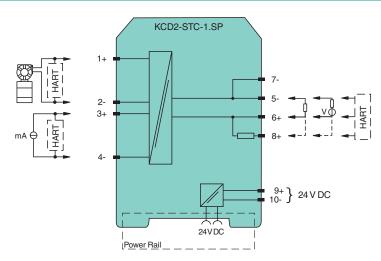
This signal conditioner provides the isolation for non-intrinsically safe applications.

The device supplies 2-wire SMART transmitters, and can also be used with 2-wire SMART current sources.

It transfers the analog input signal as an isolated current value.

Digital signals may be superimposed on the input signal and are transferred bi-directionally. Selectable output of current source, sink mode, or voltage output is available via DIP switches. If the HART communication resistance in the loop is too low, the internal resistance of 250  $\Omega$  between terminals 6 and 8 can be used. Test sockets for the connection of HART communicators are integrated into the terminals of the device.

# Connection



## **Technical Data**

General specifications		
Signal type		Analog input
Functional safety related parameters		
Safety Integrity Level (SIL)		SIL 2
Supply		
Connection		Power Rail or terminals 9+, 10-
Rated voltage	Ur	19 30 V DC
Ripple		≤ 10 %
Rated current	l <sub>r</sub>	≤ 45 mA at 24 V and 20 mA source mode output
Power dissipation		≤ 800 mW

Refer to "General Notes Relating to Pepperl+Fuchs Product Information"



# SMART Transmitter Power Supply

#### KCD2-STC-1.SP

Technical Data	
Power consumption	≤ 1.1 W
Input	
Connection side	field side
Connection	terminals 1+, 2-; 3+, 4-
Input signal	4 20 mA limited to approx. 26 mA
Open circuit voltage/short-circuit current	terminals 1+, 2-: 20 V / 26 mA
Voltage drop	terminals 3+, 4- : approx. 5 V
Available voltage	terminals 1+, 2-: $\geq$ 16 V at 20 mA ; $\geq$ 17 V at 4 mA
Output	
Connection side	control side
Connection	terminals 5-, 6+
	terminals 5-, 8+ for HART resistor
Load	$0 \dots 525 \Omega (10.5 V_{max} \text{ source mode})$
Output signal	source mode: 4 20 mA or 1 5 V (internal resistor: 250 $\Omega$ , 0.1 %) sink mode: 4 20 mA, operating voltage 5 30 V For additional internal or external loads (e. g. terminal +8) the voltage drop has to be considered, e. g. 250 $\Omega$ x 20 mA = 5 V.
Ripple	20 mV <sub>rms</sub>
Transfer characteristics	
Deviation	at 20 °C (68 °F) < 0.1 % of full scale, incl. non-linearity and hysteresis (source mode and sink mode 4 20 mA) $\leq \pm 0.2$ % incl. non-linearity and hysteresis (source mode 1 5 V)
Influence of ambient temperature	< 2 μA/K (-20 70 °C (-4 158 °F)); < 4 μA/K (-4020 °C (-404 °F)) (source mode and sink mode 4 20mA) < 0.5 mV/K (-20 70 °C (-4 158 °F)); < 1 mV/K (-4020 °C (-404 °F)) (source mode 15 V)
Frequency range	field side into the control side: bandwidth with 0.5 $V_{pp}$ signal 0 3 kHz (-3 dB) control side into the field side: bandwidth with 0.5 $V_{pp}$ signal 0 3 kHz (-3 dB)
Settling time	≤ 50 ms
Rise time/fall time	$\leq$ 10 ms
Galvanic isolation	
Input/Output	basic insulation according to IEC/EN 61010-1, rated insulation voltage 300 ${\rm V}_{\rm eff}$
Input/power supply	reinforced insulation according to IEC/EN 61010-1, rated insulation voltage 300 $V_{\text{eff}}$
Output/power supply	basic insulation according to IEC/EN 61010-1, rated insulation voltage 300 $V_{\text{eff}}$
Indicators/settings	
Display elements	LED
Control elements	DIP switch
Configuration	via DIP switches
Labeling	space for labeling at the front
Directive conformity	
Electromagnetic compatibility	
Directive 2014/30/EU	EN 61326-1:2013 (industrial locations)
Conformity	
Electromagnetic compatibility	NE 21:2017 EN 61326-3-2:2018
Degree of protection	IEC 60529:2001
Ambient conditions	
Ambient temperature	-40 70 °C (-40 158 °F)
Mechanical specifications	
Degree of protection	IP20
Connection	spring terminals
Mass	approx. 100 g
Dimensions	12.5 x 119 x 114 mm (0.5 x 4.7 x 4.5 inch) (W x H x D) , housing type A2
Mounting	on 35 mm DIN mounting rail acc. to EN 60715:2001
General information	

 Refer to "General Notes Relating to Pepperl+Fuchs Product Information".

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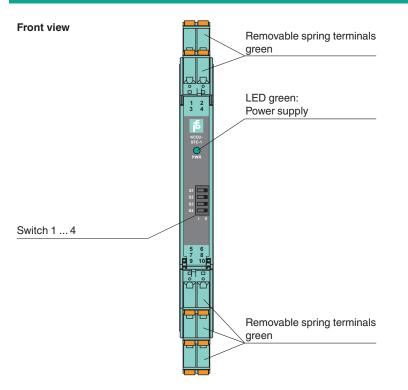
Release date: 2022-11-14 Date of issue: 2022-11-14 Filename: 321419\_eng.pdf

#### **Technical Data**

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Supplementary information
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Observe the certificates, declarations of conformity, instruction manuals, and manuals where applicable. For information see www.pepperl-fuchs.com.

#### Assembly

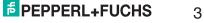


# **Matching System Components**

KFD2-EB2	Power Feed Module
UPR-03	Universal Power Rail with end caps and cover, 3 conductors, length: 2 m
UPR-03-M	Universal Power Rail with end caps and cover, 3 conductors, length: 1,6 m
UPR-03-S	Universal Power Rail with end caps and cover, 3 conductors, length: 0.8 m
K-DUCT-GY	Profile rail, wiring comb field side, gray
K-DUCT-GY-UPR-03	Profile rail with UPR-03-* insert, 3 conductors, wiring comb field side, gray

Access	ccessories					
	KC-CTT-5GN	Terminal block for KC modules, 2-pin spring terminal, with test sockets, green				
*	KF-CP	Red coding pins, packaging unit: 20 x 6				

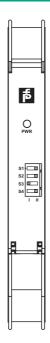
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# Application

- The device supports the following SMART protocols:
- HART
- BRAIN

### Configuration



#### **Output switch settings**

Mode of operation	S1	S2	S3	S4
Current source output 4 20 mA	Ш	Ш	I	II
Voltage source output 1 5 V	Ш	Ш	I	I
Current sink output 4 20 mA	11	I	=	=

Factory setting: current source output 4 ... 20 mA

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